

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Hosein, et al.

Serial No.: 10/731,909

Filed: December 10, 2003

For: **PER USER RATE CONTROL FOR THE  
REVERSE LINK IN CDMA NETWORKS**

Attorney's Docket No.: 4740-231



Examiner TBA  
Group Art Unit TBA

Raleigh, North Carolina  
May 6, 2004

Mail Stop Patent Applications  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Information Disclosure Statement**

Sir:

Attached hereto is an information disclosure statement (PTO-1449). This IDS is being submitted before any action on the merits. As such, no fee is required. However, if fees are required, please deduct from our Deposit Account No. 18-1167.

Respectfully submitted,

**COATS AND BENNETT, PLLC**

By:

  
Michael D. Murphy  
Registration No. 44,958

Telephone: (919) 854-1844

**CERTIFICATE OF MAILING**

I HEREBY CERTIFY THAT THIS DOCUMENT IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL, POSTAGE PREPAID, ON THE DATE INDICATED, IN AN ENVELOPE ADDRESSED TO MAIL STOP PATENT APPLICATION, COMMISSIONER FOR PATENTS, P.O. BOX 1450, WASHINGTON, DC 22313-1450.

SIGNATURE: Michael D. Murphy

DATE: 5-6-04



---

**FORM PTO-1449**

**INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION PURSUANT TO  
37 CFR §1.97 & 1.98**

| Docket Number: | Serial Number:  
| 4740-231 | 10/731 909

I Applicant:  
I Hosein et al.

I Filing Date: I Group:  
I December 10, 2003 I

## U. S. PATENT DOCUMENTS

Examiner Initial	Patent No.	Date	Name	Class	Subcl	Filing Date If Approp.

## FOREIGN PATENT DOCUMENTS

						Translation	
	Patent No.	Date	Name	Class	Subcl	Yes	No

**OTHER DOCUMENTS** (including author, title, date, pages, etc.)

no date —

Incomplete; Farooq Khan; System Performance of Lucent's HRPD  
Proposal; C30-20030915-217; 3 pgs

no date —

**electronics: Kwon, et al - Description of Scheduling and Resource Allocation Mechanisms**

Ir Various Control Mechanisms simulated by Samsung under HARQ  
30-WG3-20030812-071; 14 pgs.

/QV/ | Full Buffer Performance of 5ms, 4 transmission and 10ms, 3 transmission with distributed scheduling using geometry based-priority function; | August 12 2003; C30-20030812-061-QCOM: 4 pos.

/QV/ D LG Electronics; Kim, et al.; Rate Control Mechanism for LGE's Reverse Link Framework; Proposal for Revision D; August 6, 2002; C20-2203-806-021; 9 pages.

Framework Proposal for Revision D, August 6, 2003, C50-2203-800-021, 9 pgs.

no date

| Transmission and Full Buffer Traffic; August 6, 2003; C30-20030806-042; 9 pgs.  
| Nortel Networks; Vrzie, et al.; Full Buffer Simulation Results for Semi-distributed  
| Scheduling, with 10ms Frame and 3 Transmissions; C30-2003-806-052R1; 2 pgs.

EXAMINER /Quochien Vuong/ DATE CONSIDERED /

**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation not in conformance and not considered. Include copy of this form with next communication to the applicant.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /QV/